IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

| Priority Application Serial No | |
|--|--------------------------|
| Priority Filing Date | |
| Inventor | Vishnu K. Agarwal, et al |
| Assignee | Micron Technology, Inc. |
| Priority Group Art Unit | 2813 |
| Priority Examiner | Y. Huynh |
| Attorney's Docket No | |
| Title: Capacitor Fabrication Methods and Capacitor Constructions | |

LETTER SUBMITTING FORMAL DRAWINGS

Assistant Commissioner for Patents Attention: Official Draftsman Washington, D.C. 20231

Please enter the enclosed formal drawings in the above-referenced application in place of drawings originally filed.

Acknowledgment of receipt of the formal drawings and their acceptance into the file is requested.

Respectfully submitted,

Date: 15 Jan 2002 By:

James E. Lake / Reg. No.: 44,854

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Enclosures: Five (5) Sheets of Formal Drawings (Figs. 1-10)